

**A METHOD OF IDENTIFYING AN ACCURATE MODEL****ABSTRACT**

A method is described for identifying an inaccurate model of a hardware circuit. The method includes the steps of simulating the model of the circuit by applying a plurality of signals, said plurality of signals having at least one abstract data type level to provide a set of expected results, replacing the ~~or each~~ abstract data type level with two or more levels having different values to thereby provide an expanded set of signals to apply to said model, resimulating the model with said expanded set and comparing the two sets of results and providing an output signal indicating if the model is inaccurate if the results contradict.

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